

image

2827

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

James M. Derderian

Serial No.: 09/939,258

Filed: August 24, 2001

For: SEMICONDUCTOR DEVICES
INCLUDING STACKING SPACERS
THEREON, ASSEMBLIES INCLUDING
THE SEMICONDUCTOR DEVICES AND
METHODS

Examiner: D. Graybill

Group Art Unit: 2827

Attorney Docket No.: 2269-4831US
(01-0105.00/US)

CERTIFICATE OF MAILING

I hereby certify that this correspondence along with any attachments referred to or identified as being attached or enclosed is being deposited with the United States Postal Service as First Class Mail on the date of deposit shown below with sufficient postage and in an envelope addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

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SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

In compliance with the duty to disclose information material to patentability pursuant to 37 C.F.R. § 1.56, it is respectfully requested that this Supplemental Information Disclosure Statement be entered and the documents listed on attached Form PTO-1449 or PTO/SB/08 be considered by the Examiner and made of record. Copies of the listed documents are enclosed pursuant to 37 C.F.R. § 1.98(a).

In accordance with 37 C.F.R. § 1.97(g) and (h), filing of this Supplemental Information Disclosure Statement is not to be construed as a representation that a search has been made or an admission that the information cited herein is, or is considered to be, material to patentability as defined in 37 C.F.R. § 1.56(b). Further, no representation is made by Applicant herein that no other possible material information as defined in 37 C.F.R. § 1.56 (b) exists.

U.S. Patent Documents

<u>U.S. Patent No.</u>	<u>Publication Date</u>	<u>Patentee</u>
3,811,186	05/21/74	Larnerd et al.
3,871,015	03/11/75	Lin et al.
5,056,215	10/15/91	Blanton
5,220,200	06/15/93	Blanton
5,484,314	01/16/96	Farnworth
5,633,535	05/27/97	Chao et al.
5,700,715	12/23/97	Pasch
5,841,198	11/24/98	Chia et al.
5,985,682	11/16/99	Higgins, III
6,271,598	08/07/01	Vindasius et al.
6,281,577	08/28/01	Oppermann et al.
6,287,895	09/11/01	Sato
6,046,910	04/04/00	Ghaem et al.
6,410,366	06/25/02	Hashimoto
6,461,881	10/08/02	Farnworth et al.
6,630,365	10/07/03	Farnworth et al.
6,649,444	11/18/03	Earnworth et al.

Foreign Patent Documents

<u>Document No.</u>	<u>Publication Date</u>	<u>Patentee</u>
JP 357176738A	10/30/1982	Sakuma
JP 358157146A	09/19/1983	Watanabe
JP 01238148A	09/22/1989	Masato
EP 0463297A1	03/23/1991	Kuke
JP 404037137A	02/07/1992	Tate
JP 404062866A	02/27/1992	Yamashita
JP 11214432A	01/28/1998	Atsushi
JP 10189653A	07/21/1998	Kuniaki
JP 411040608A	02/12/1999	Kikuchi
JP 2000077458A	03/14/2000	Kuzuhara
JP 2000299330A	10/24/2000	Yamaguchi

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Other Documents

U.S. Patent Application Publication 2002/0043711 A1 to Akram et al., dated April 18, 2002

U.S. Patent Application Publication 2002/0068417 A1 to Farnworth et al., dated June 6, 2002

U.S. Patent Application Publication 2002/0185746 A1 to Park, dated December 12, 2002

U.S. Patent Application Publication 2003/0022462 A1 to Farnworth et al., dated January 30, 2003

U.S. Patent Application Publication 2003/0170921 A1 to Akram, dated September 11, 2003

U.S. Patent Application Serial No. 09/590,527, filed June 8, 2000, entitled "Structures for Stabilizing Semiconductor Devices Relative to Test Substrates and Methods for Fabricating the Stabilizers", inventor Salman Akram

U.S. Patent Application Serial No. 09/590,621, filed June 8, 2000, entitled "Stereolithographic Method and Apparatus for Fabricating Stabilizers for Flip-Chip Type Semiconductor Devices and Resulting Structures", inventor Akram et al.

Applicant offers to supply any explanation or discussion of the documents which the Examiner feels is necessary or desirable and which is requested.

Serial No. 09/939,258

This Supplemental Information Disclosure Statement is filed before the mailing date of a first Office Action on the merits.

Respectfully submitted,

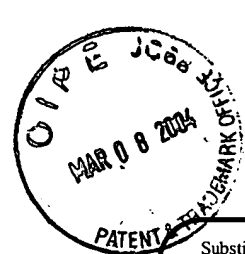


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Date: March 4, 2004
BGP/sls:djp

Enclosures: Form PTO-1449 or PTO/SB/08
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**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

(use as many sheets as necessary)

Sheet 1

of

2

Complete if Known

Application Number	09/939,258
Filing Date	August 24, 2001
First Named Inventor	James M. Derderian
Group Art Unit	2827
Examiner Name	D. Graybill
Attorney Docket Number	4831US (01-0105.00/US)

U.S. PATENT DOCUMENTS

Examiner Initials *	Cite No. ¹	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number - Kind Code ² (if known)			
		US-3,811,186	05/21/74	Larned et al.	
		US- 3,871,015	03/11/75	Lin et al.	
		US- 5,056,215	10/15/91	Blanton	
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		US- 5,633,535	05/27/97	Chao et al.	
		US- 5,700,715	12/23/97	Pasch	
		US- 5,841,198	11/24/98	Chia et al.	
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		US- 6,271,598	08/07/01	Vindasius et al.	
		US- 6,281,577	08/28/01	Oppermann et al.	
		US- 6,287,895	09/11/01	Sato	
		US- 6,046,910	04/04/00	Ghaem et al.	
		US- 6,410,366	06/25/02	Hashimoto	
		US- 6,461,881	10/08/02	Farnworth et al.	
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		US- 6,649,444	11/18/03	Earnworth et al.	
		US-			

FOREIGN PATENT DOCUMENTS

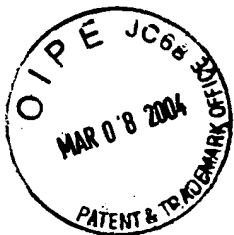
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		Country Code ³ - Number ⁴ - Kind Code ⁵ (if known)				
		JP 357176738A	10/30/1982	Sakuma		
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		JP 2000077458A	03/14/2000	Kuzuhara		
		JP 2000299330A	10/24/2000	Yamaguchi		

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¹ Applicant's unique citation designation number (optional). ² See Kinds Codes of USPTO Patent Documents at www.uspto.gov or MPEP 901.04. ³ Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). ⁴ For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. ⁵ Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible. ⁶ Applicant is to place a check mark here if English language Translation is attached.

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PTO/SB/08B(10-01)

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Sheet 2 of 2

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Group Art Unit	2827
Examiner Name	D. Graybill
Attorney Docket Number	48311US (01-0105 00/LIS)

OTHER PRIOR ART -- NON PATENT LITERATURE DOCUMENTS

Examiner Initials *	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
		U.S. Patent Application Publication 2002/0043711 A1 to Akram et al., dated April 18, 2002	
		U.S. Patent Application Publication 2002/0068417 A1 to Farnworth et al., dated June 6, 2002	
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